

ABSTRACT OF THE DISCLOSURE

A display panel includes a plurality of scanning lines; a plurality of signal lines, each of the signal lines transmitting a multiplexed data signal for first to third color components; a plurality of pixels, each of the pixels being connected with one of the scanning lines and one of the signal lines; and a plurality of demultiplexers, each of the demultiplexers including first to third demultiplex switching elements which are respectively switch-controlled based on first to third demultiplex control signals, one end of each of the first to third demultiplex switching elements being connected with one of the signal lines and the other end of each of the first to third demultiplex switching elements being connected with one of the pixels for a j-th color component ($1 \leq j \leq 3$, j is an integer). A gate signal corresponding to shift output obtained by shifting a start pulse signal which is generated when at least two of the first to third demultiplex control signals go active at the same time is output to each of the scanning lines.